

## AT32F415 AT32F421 ERTC Timestamp

### Introduction

This example code demonstrates how to use ERTC timestamp function in AT32F415 and AT32F421.

*Note: This sample code is written based on Artery's V2.x.x BSP. For other versions of BSP, users should pay attention to the differences in use.*

Applicable products:

Product series	AT32F415xx
	AT32F421xx

List of major peripherals used:

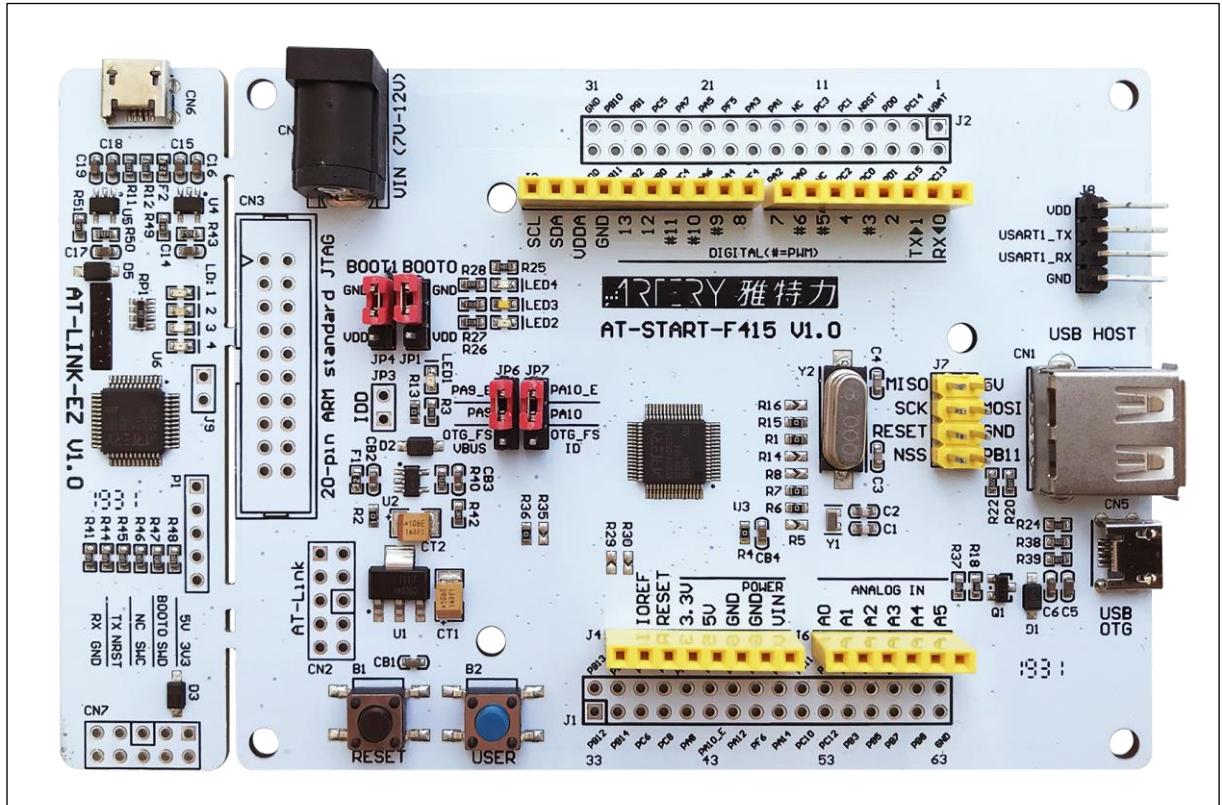
Peripherals	ERTC
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## 1 Quick start

### 1.1 Hardware resources

- 1) AT-START-F415 V1.0 and AT-START-F421 V1.0 evaluation boards
- 2) Serial interfaces: PA9, PA10

Figure 1. AT-START-F415 V1.0 evaluation board



### 1.2 Software resources

- 1) SourceCode
  - ERTC\_TimeStamp

*Note: All of projects are built based on Keil 5. For the need to run them in other compiling environments, user can make simple adjustments according to AT32xxx\_Firmware\_Library\_V2.x.x\project\at\_start\_xxx\templates.*

### 1.3 Example case

- 1) Open "ERTC\_TimeStamp", compile and download it to the evaluation board
- 2) If AT-START-F421 V1.0 board is to be used, please choose AT\_START\_421 project
- 3) When a rising edge is detected on PC13, a timestamp event is generated, and time will be saved in a timestamp register. Timestamp data can be printed out through serial interfaces.

Figure 2. Timestamp information print



## 2 Revision history

Table 1. Document revision history

Date	Revision	Changes
2021.12.20	2.0.0	Initial release

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